IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Sawey et al.

Assignee: Vitesse Semiconductor Corporation

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 DATA LOOP PORT ACCELERATION CIRCUIT

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I hereby certify that the enclosed <u>Issue Fee Transmittal; and Payment by Credit Card</u> are being transmitted to the United States Patent and Trademark Office via EFS-Web on <u>May 5, 2009</u>.

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